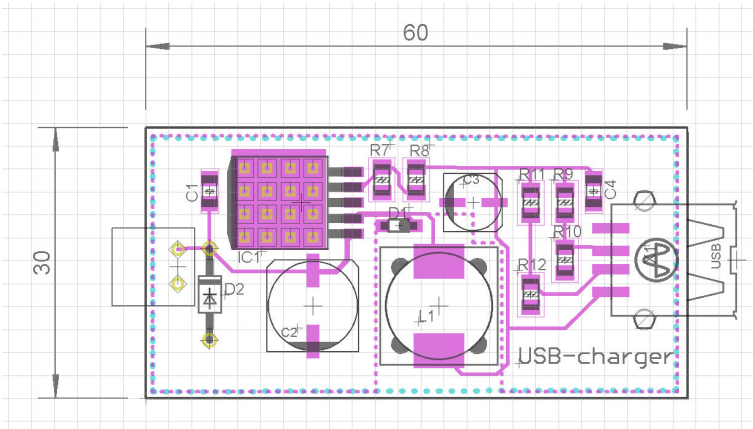
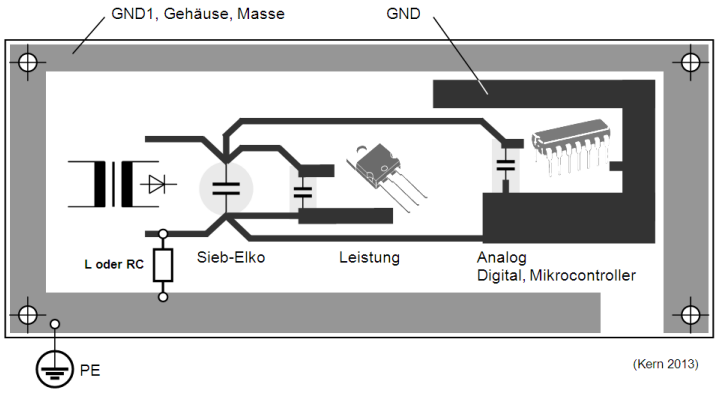
PCB-Layout, 10 Punkte Memo

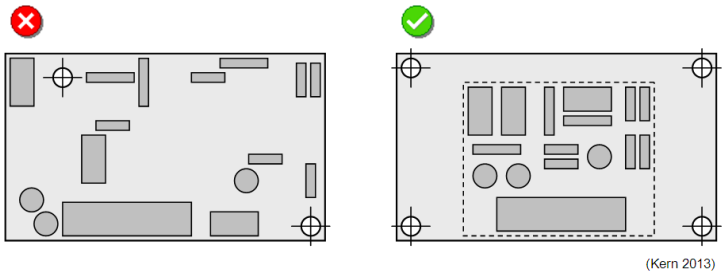
2014 ke



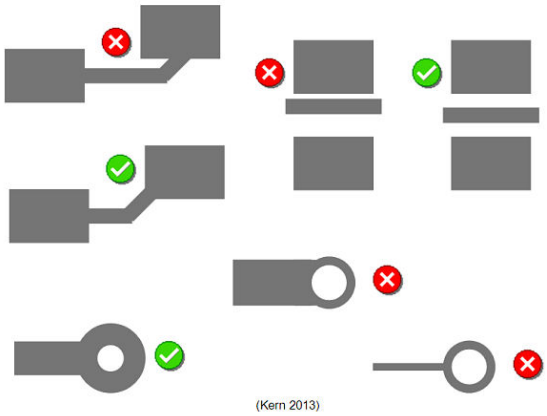
1 - Raster/Grid verwenden:
Mechanik/Umrisse in mm - Platzierung/Routing in mil



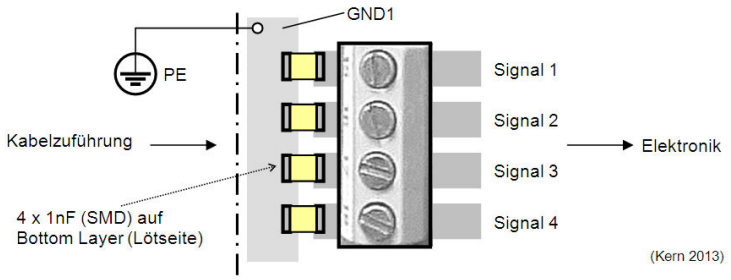
2 - Platzierung, Ground-Systeme



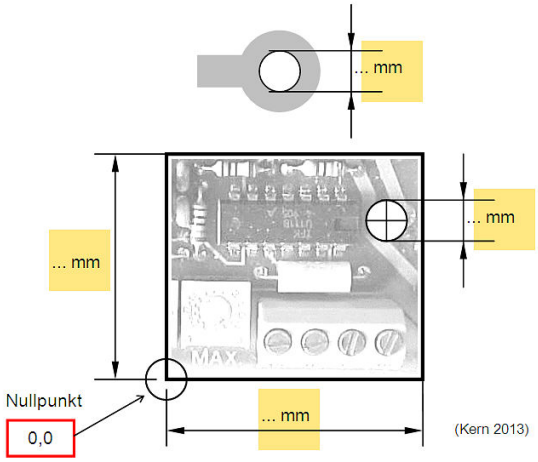
3 - Platzierung, Verteilung der Komponenten



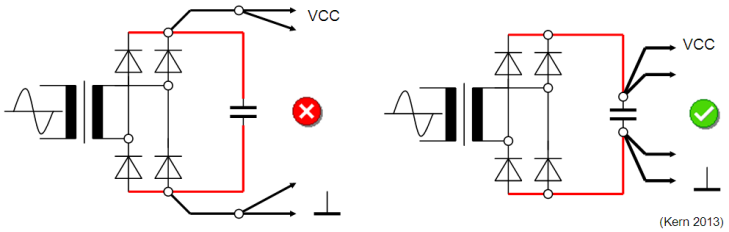
4 - Kupfer-Geometrien und Restringe



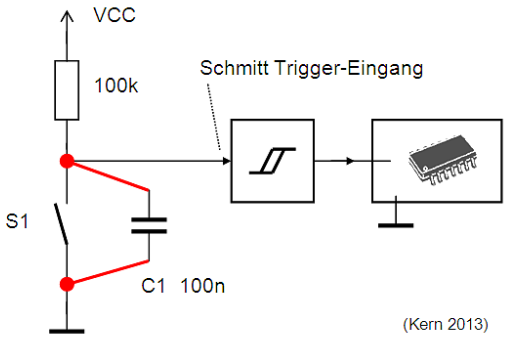
5 - EMV-Filter-Cs an Ein-/Ausgängen



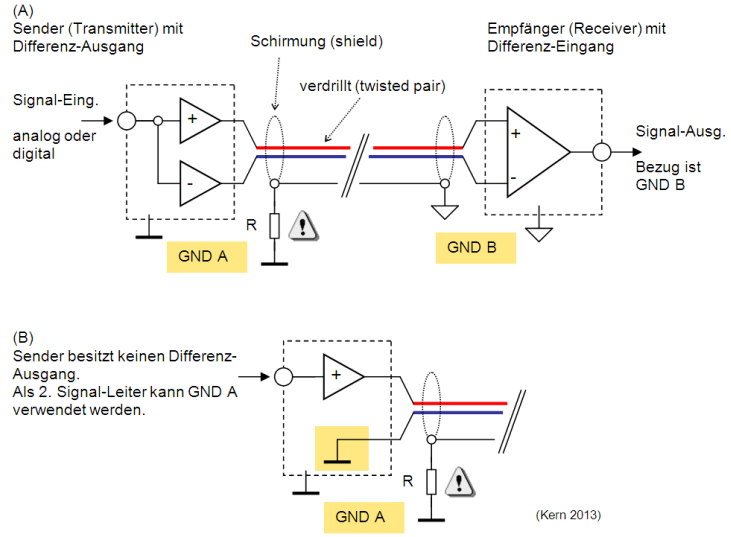
6 - Mechanik in mm - Bauteil-Platzierung, Leiter und Pads in mil



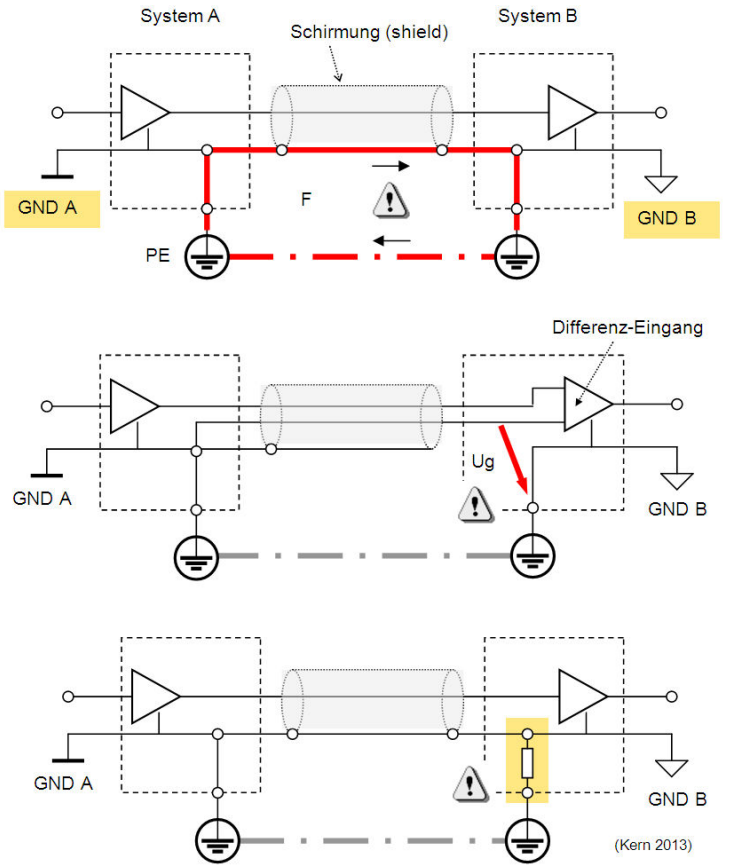
7 - Leiterführung bei hohen Rippelströmen im Netzteil



8 - Leiterführung bei geschalteten Cs



9 - Symmetrische Signalführung, +/- Signale



10 - GND-Ausgleichsströme (GND-Loops) vermeiden