

The open drain port configuration is shown in Figure 5-2.

An open drain port pin has a Schmitt-triggered input that also has a glitch suppression circuit.

Please refer to the **P89LPC933/934/935** datasheet, AC Electrical Characteristics for glitch filter specifications).

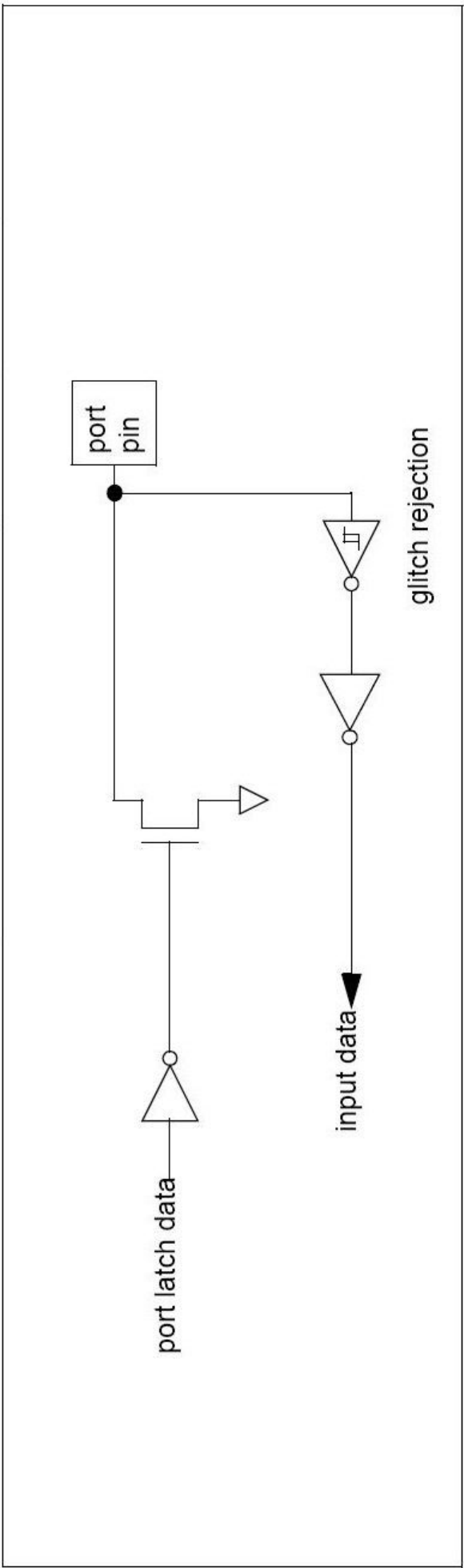


Figure 5-2: Open drain output