

The quasi-bidirectional port configuration is shown in Figure 5-1.

Although the **P89LPC933/934/935** is a 3 V device most of the pins are 5 V-tolerant. If 5 V is applied to a pin configured in quasi-bidirectional mode, there will be a current flowing from the pin to V_{DD} causing extra power consumption. Therefore, applying 5 V to pins configured in quasi-bidirectional mode is discouraged.

A quasi-bidirectional port pin has a Schmitt-triggered input that also has a glitch suppression circuit.

(Please refer to the **P89LPC933/934/935** datasheet, AC Electrical Characteristics for glitch filter specifications)

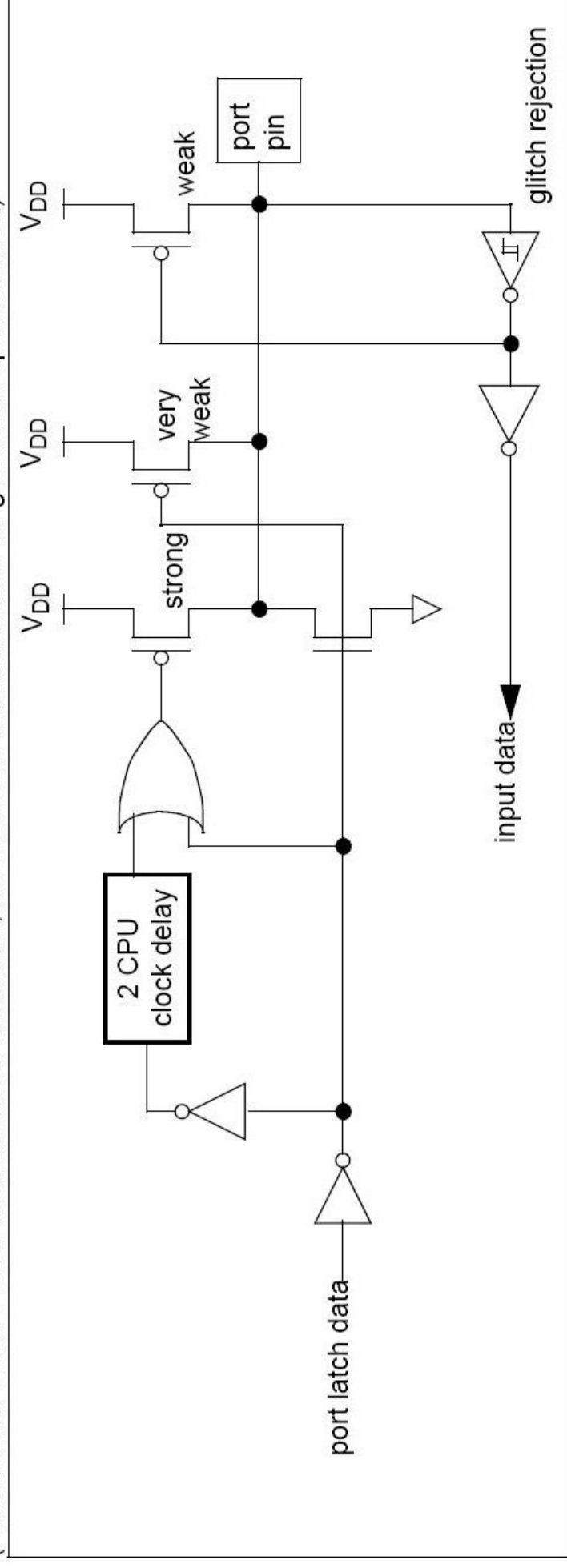


Figure 5-1: Quasi-bidirectional output