

The push-pull port configuration is shown in Figure 5-4.

A push-pull port pin has a Schmitt-triggered input that also has a glitch suppression circuit.

(Please refer to the **P89LPC933/934/935** datasheet, AC Electrical Characteristics for glitch filter specifications)

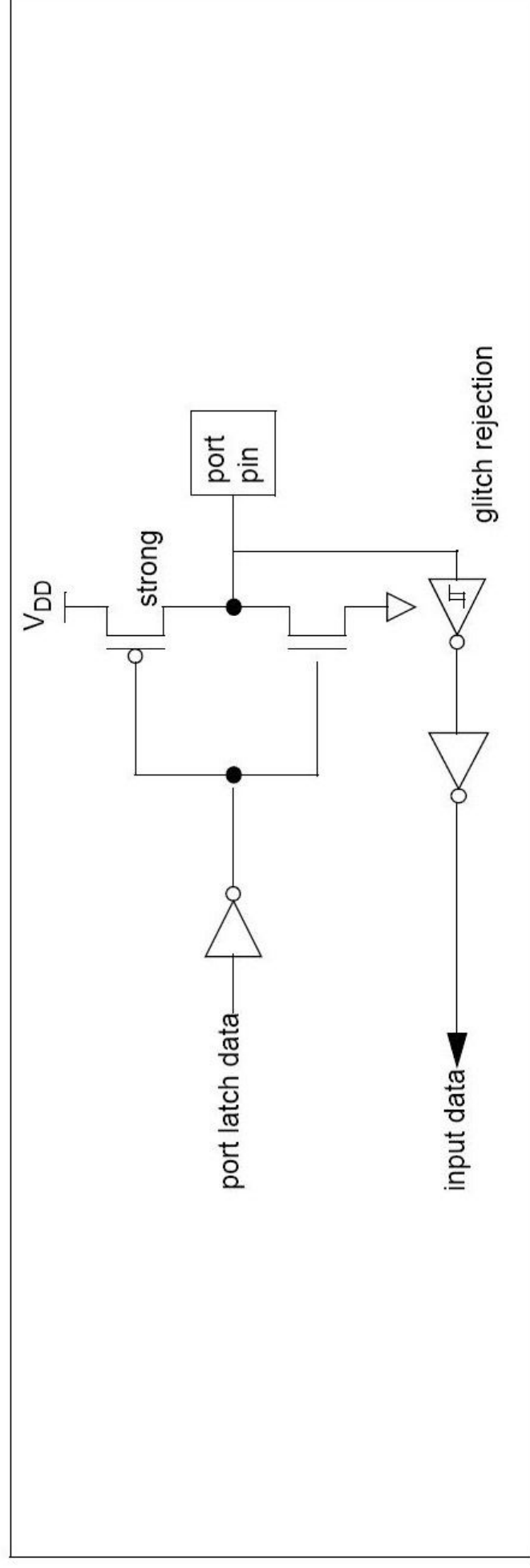


Figure 5-4: Push-pull output