

The input port configuration is shown in Figure 5-3. It is a Schmitt-triggered input that also has a glitch suppression circuit. (Please refer to the **P89LPC933/934/935** datasheet, AC Electrical Characteristics for glitch filter specifications)

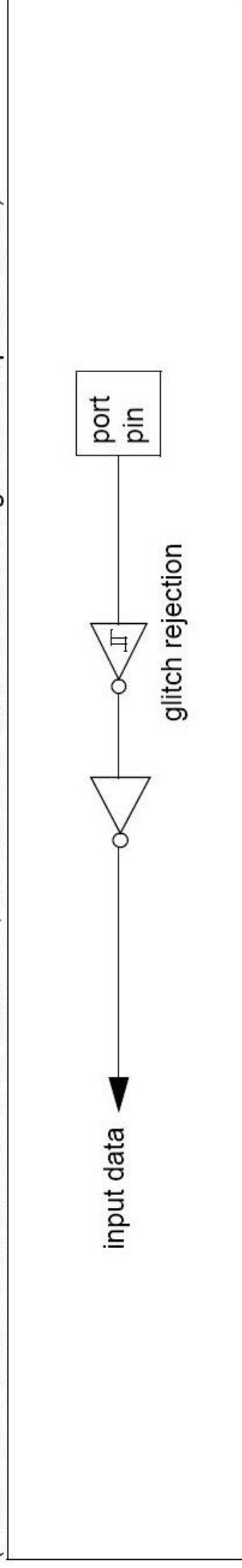


Figure 5-3: Input -only