November 2000



LM317L 3-Terminal Adjustable Regulator

General Description

The LM317L is an adjustable 3-terminal positive voltage regulator capable of supplying 100mA over a 1.2V to 37V output range. It is exceptionally easy to use and requires only two external resistors to set the output voltage. Further, both line and load regulation are better than standard fixed regulators. Also, the LM317L is available packaged in a standard TO-92 transistor package which is easy to use.

In addition to higher performance than fixed regulators, the LM317L offers full overload protection. Included on the chip are current limit, thermal overload protection and safe area protection. All overload protection circuitry remains fully functional even if the adjustment terminal is disconnected.

Normally, no capacitors are needed unless the device is situated more than 6 inches from the input filter capacitors in which case an input bypass is needed. An optional output capacitor can be added to improve transient response. The adjustment terminal can be bypassed to achieve very high ripple rejection ratios which are difficult to achieve with standard 3-terminal regulators.

Besides replacing fixed regulators, the LM317L is useful in a wide variety of other applications. Since the regulator is "floating" and sees only the input-to-output differential voltage, supplies of several hundred volts can be regulated as long as the maximum input-to-output differential is not exceeded.

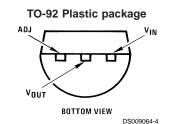
Also, it makes an especially simple adjustable switching regulator, a programmable output regulator, or by connecting a fixed resistor between the adjustment and output, the LM317L can be used as a precision current regulator. Supplies with electronic shutdown can be achieved by clamping the adjustment terminal to ground which programs the output to 1.2V where most loads draw little current.

The LM317L is available in a standard TO-92 transistor package, the SO-8 package, and 6-Bump micro SMD package. The LM317L is rated for operation over a -25° C to 125°C range.

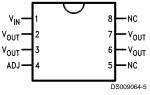
Features

- Adjustable output down to 1.2V
- Guaranteed 100 mA output current
- Line regulation typically 0.01%V
- Load regulation typically 0.1%
- Current limit constant with temperature
- Eliminates the need to stock many voltages
- Standard 3-lead transistor package
- 80 dB ripple rejection
- Available in TO-92, SO-8, or 6-Bump micro SMD package
- Output is short circuit protected
- See AN-1112 for micro SMD considerations

Connection Diagrams

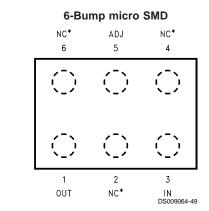


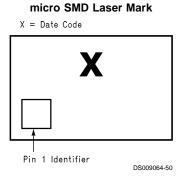
8-Pin SOIC



Top View

Connection Diagrams (Continued)





*NC = Not Internally connected.

Top View (Bump Side Down)

Package	Part Number	Package Marking	Media Transport	NSC Drawing	
TO-92	LM317LZ	LM317LZ	1.8k Units per Box	Z03A	
8-Pin SOIC	LM317LM	LM317LM	Rails	M08A	
6-Bump micro	* LM317LIBP	-	250 Units Tape and Reel	BPA06HPB	
SMD	* LM317LIBPX	-	3k Units Tape and Reel		

Note: The micro SMD package marking is a single digit manufacturing Date Code only.

Absolute Maximum Ratings (Note 1)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/ Distributors for availability and specifications.

Storage Temperature
Lead Temperature
(Soldering, 4 seconds)
Output is Short Circuit Protect
ESD rating to be determined

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260°C

LM317L

Power Dissipation	Internally Limited
Input-Output Voltage Differential	40V
Operating Junction Temperature Range	-40°C to +125°C

Electrical Characteristics (Note 2)

ted ESD rating to be determined.

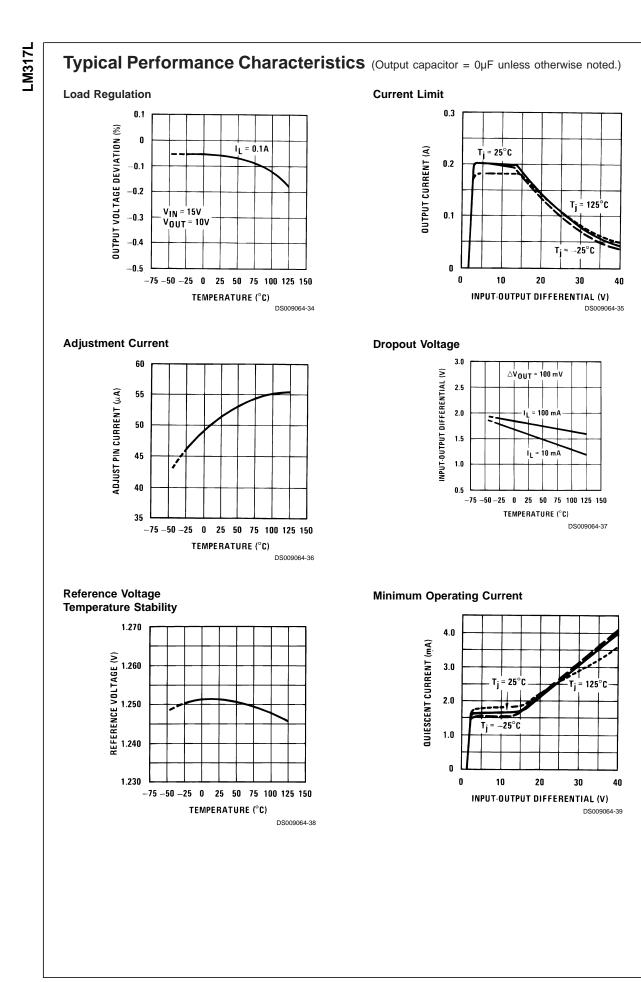
Parameter	Conditions	Min	Тур	Max	Units
Line Regulation	$T_J = 25^{\circ}C, \ 3V \le (V_{IN} - V_{OUT}) \le 40V, \ I_L \le 20mA \ (Note \ 3)$		0.01	0.04	%/V
Load Regulation	$T_J = 25^{\circ}C, 5mA \le I_{OUT} \le I_{MAX}, (Note 3)$		0.1	0.5	%
Thermal Regulation	$T_J = 25^{\circ}C$, 10ms Pulse		0.04	0.2	%/W
Adjustment Pin Current			50	100	μA
Adjustment Pin Current	$5mA \le I_L \le 100mA$		0.2	5	μA
Change	$3V \le (V_{IN} - V_{OUT}) \le 40V, P \le 625mW$				
Reference Voltage	$3V \le (V_{IN} - V_{OUT}) \le 40V$, (Note 4)	1.20	1.25	1.30	V
	$5mA \le I_{OUT} \le 100mA$, P $\le 625mW$				
Line Regulation	$3V \le (V_{IN} - V_{OUT}) \le 40V, I_L \le 20mA$ (Note 3)		0.02	0.07	%/V
Load Regulation	$5mA \le I_{OUT} \le 100mA$, (Note 3)		0.3	1.5	%
Temperature Stability	$T_{MIN} \le T_J \le T_{Max}$		0.65		%
Minimum Load Current	$(V_{IN} - V_{OUT}) \le 40V$		3.5	5	mA
	$3V \le (V_{IN} - V_{OUT}) \le 15V$		1.5	2.5	
Current Limit	$3V \le (V_{IN} - V_{OUT}) \le 13V$	100	200	300	mA
	$(V_{IN} - V_{OUT}) = 40V$	25	50	150	mA
Rms Output Noise, % of V_{OUT}	$T_J = 25^{\circ}C, 10Hz \le f \le 10kHz$		0.003		%
Ripple Rejection Ratio	$V_{OUT} = 10V, f = 120Hz, C_{ADJ} = 0$		65		dB
	$C_{ADJ} = 10 \mu F$	66	80		dB
Long-Term Stability	T _J = 125°C, 1000 Hours		0.3	1	%
Thermal Resistance	Z Package 0.4" Leads		180		°C/W
Junction to Ambient	Z Package 0.125 Leads		160		°C/W
	SO-8 Package		165		°C/W
	6-Bump micro SMD		290		°C/W
Thermal Rating of SO Package			165		°C/W

Note 1: "Absolute Maximum Ratings" indicate limits beyond which damage to the device may occur. Operating Ratings indicate conditions for which the device is functional, but do not guarantee specific performance limits.

Note 2: Unless otherwise noted, these specifications apply: $-25^{\circ}C \le T_j \le 125^{\circ}C$ for the LM317L; $V_{IN} - V_{OUT} = 5V$ and $I_{OUT} = 40$ mA. Although power dissipation is internally limited, these specifications are applicable for power dissipations up to 625 mW. I_{MAX} is 100 mA.

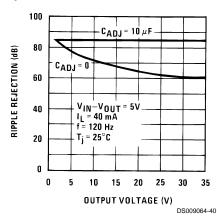
Note 3: Regulation is measured at constant junction temperature, using pulse testing with a low duty cycle. Changes in output voltage due to heating effects are covered under the specification for thermal regulation.

Note 4: Thermal resistance of the TO-92 package is 180°C/W junction to ambient with 0.4" leads from a PC board and 160°C/W junction to ambient with 0.125" lead length to PC board.

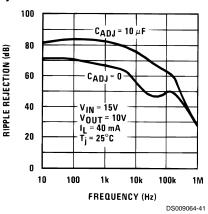


Typical Performance Characteristics (Output capacitor = 0µF unless otherwise noted.) (Continued)

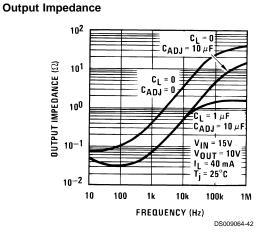
Ripple Rejection



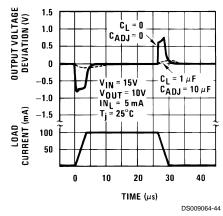
Ripple Rejection





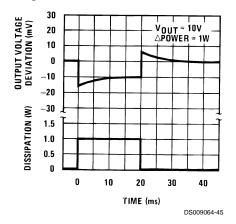


Load Transient Response



1.5 OUTPUT VOLTAGE DEVIATION (V) Ċ_L = 0 1.0 C_L = 1 μF C_{ADJ} = 10 μF C_{ADJ} = 0 0.5 0 -0.5 V_{IN} = 15V VOUT = 10V IL = 40 mA T_j = 25°C -1.0 -1.5 INPUT VOLTAGE CHANGE (V) 1.0 0.5 0 0 2 4 6 8 10 12 14 16 TIME (µs) DS009064-43

Thermal Regulation

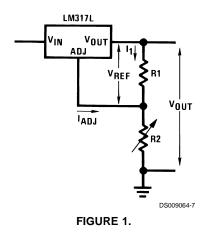


Application Hints

In operation, the LM317L develops a nominal 1.25V reference voltage, V_{REF} , between the output and adjustment terminal. The reference voltage is impressed across program resistor R1 and, since the voltage is constant, a constant current I_1 then flows through the output set resistor R2, giving an output voltage of

$$V_{OUT} = V_{REF} \left(1 + \frac{R2}{R1} \right) + I_{ADJ}(R2)$$

Since the 100 μ A current from the adjustment terminal represents an error term, the LM317L was designed to minimize I_{ADJ} and make it very constant with line and load changes. To do this, all quiescent operating current is returned to the output establishing a minimum load current requirement. If there is insufficient load on the output, the output will rise.



External Capacitors

An input bypass capacitor is recommended in case the regulator is more than 6 inches away from the usual large filter capacitor. A 0.1μ F disc or 1μ F solid tantalum on the input is suitable input bypassing for almost all applications. The device is more sensitive to the absence of input bypassing when adjustment or output capacitors are used, but the above values will eliminate the possibility of problems.

The adjustment terminal can be bypassed to ground on the LM317L to improve ripple rejection and noise. This bypass capacitor prevents ripple and noise from being amplified as the output voltage is increased. With a 10μ F bypass capacitor 80 dB ripple rejection is obtainable at any output level. Increases over 10μ F do not appreciably improve the ripple rejection at frequencies above 120Hz. If the bypass capacitor is used, it is sometimes necessary to include protection diodes to prevent the capacitor from discharging through internal low current paths and damaging the device.

In general, the best type of capacitors to use is solid tantalum. Solid tantalum capacitors have low impedance even at high frequencies. Depending upon capacitor construction, it takes about 25µF in aluminum electrolytic to equal 1µF solid tantalum at high frequencies. Ceramic capacitors are also good at high frequencies; but some types have a large decrease in capacitance at frequencies around 0.5MHz. For this reason, a 0.01µF disc may seem to work better than a 0.1µF disc as a bypass.

Although the LM317L is stable with no output capacitors, like any feedback circuit, certain values of external capacitance can cause excessive ringing. This occurs with values between 500 pF and 5000 pF. A $1\mu F$ solid tantalum (or $25\mu F$ aluminum electrolytic) on the output swamps this effect and insures stability.

Load Regulation

The LM317L is capable of providing extremely good load regulation but a few precautions are needed to obtain maximum performance. The current set resistor connected between the adjustment terminal and the output terminal (usually 240 Ω) should be tied directly to the output of the regulator rather than near the load. This eliminates line drops from appearing effectively in series with the reference and degrading regulation. For example, a 15V regulator with 0.05 Ω resistance between the regulator and load will have a load regulation due to line resistance of $0.05\Omega \times I_L$. If the set resistor is connected near the load the effective line resistance will be 0.05Ω (1 + R2/R1) or in this case, 11.5 times worse.

Figure 2 shows the effect of resistance between the regulator and 240Ω set resistor.

With the TO-92 package, it is easy to minimize the resistance from the case to the set resistor, by using two separate leads to the output pin. The ground of R2 can be returned near the ground of the load to provide remote ground sensing and improve load regulation.

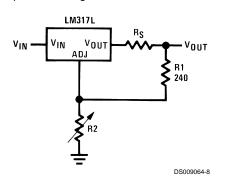


FIGURE 2. Regulator with Line Resistance in Output Lead

Thermal Regulation

When power is dissipated in an IC, a temperature gradient occurs across the IC chip affecting the individual IC circuit components. With an IC regulator, this gradient can be especially severe since power dissipation is large. Thermal regulation is the effect of these temperature gradients on output voltage (in percentage output change) per watt of power change in a specified time. Thermal regulation error is independent of electrical regulation or temperature coefficient, and occurs within 5ms to 50ms after a change in power dissipation. Thermal regulation depends on IC layout as well as electrical design. The thermal regulation of a voltage regulator is defined as the percentage change of V_{OUT} , per watt, within the first 10ms after a step of power is applied. The LM317L specification is 0.2%/W, maximum.

In the Thermal Regulation curve at the bottom of the Typical Performance Characteristics page, a typical LM317L's output changes only 7mV (or 0.07% of $V_{OUT} = -10V$) when a 1W pulse is applied for 10 ms. This performance is thus well inside the specification limit of 0.2%/W x 1W = 0.2% maximum. When the 1W pulse is ended, the thermal regulation again shows a 7 mV change as the gradients across the LM317L chip die out. Note that the load regulation error of about 14 mV (0.14%) is additional to the thermal regulation error.

Application Hints (Continued)

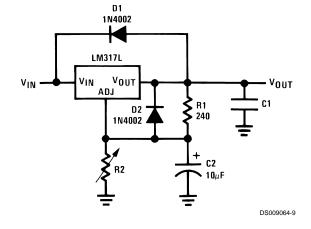
Protection Diodes

When external capacitors are used with *any* IC regulator it is sometimes necessary to add protection diodes to prevent the capacitors from discharging through low current points into the regulator. Most 10μ F capacitors have low enough internal series resistance to deliver 20A spikes when shorted. Although the surge is short, there is enough energy to damage parts of the IC.

When an output capacitor is connected to a regulator and the input is shorted, the output capacitor will discharge into the output of the regulator. The discharge current depends on the value of the capacitor, the output voltage of the regulator, and the rate of decrease of $V_{\rm IN}$. In the LM317L,

this discharge path is through a large junction that is able to sustain a 2A surge with no problem. This is not true of other types of positive regulators. For output capacitors of 25 μF or less, the LM317L's ballast resistors and output structure limit the peak current to a low enough level so that there is no need to use a protection diode.

The bypass capacitor on the adjustment terminal can discharge through a low current junction. Discharge occurs when *either* the input or output is shorted. Internal to the LM317L is a 50 Ω resistor which limits the peak discharge current. No protection is needed for output voltages of 25V or less and 10 μ F capacitance. *Figure 3* shows an LM317L with protection diodes included for use with outputs greater than 25V and high values of output capacitance.



 $V_{OUT} = 1.25V \left(1 + \frac{R^2}{R^1}\right) I_{ADJ} R^2$ D1 protects against C1

D2 protects against C2

FIGURE 3. Regulator with Protection Diodes

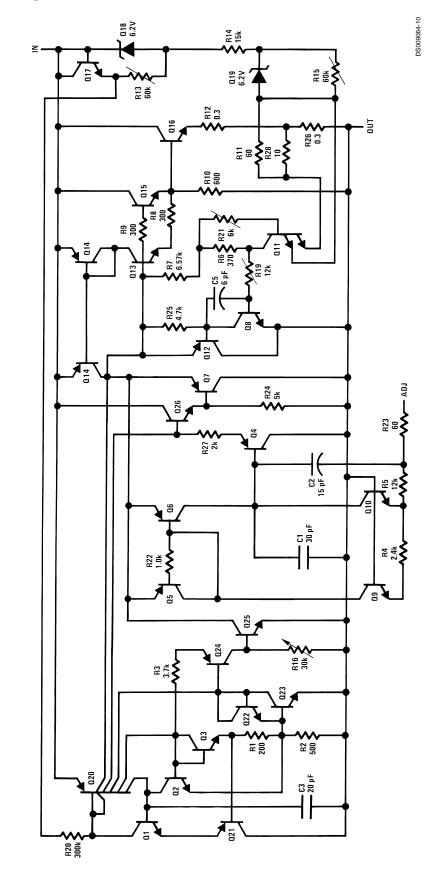
LM317L micro SMD Light Sensitivity

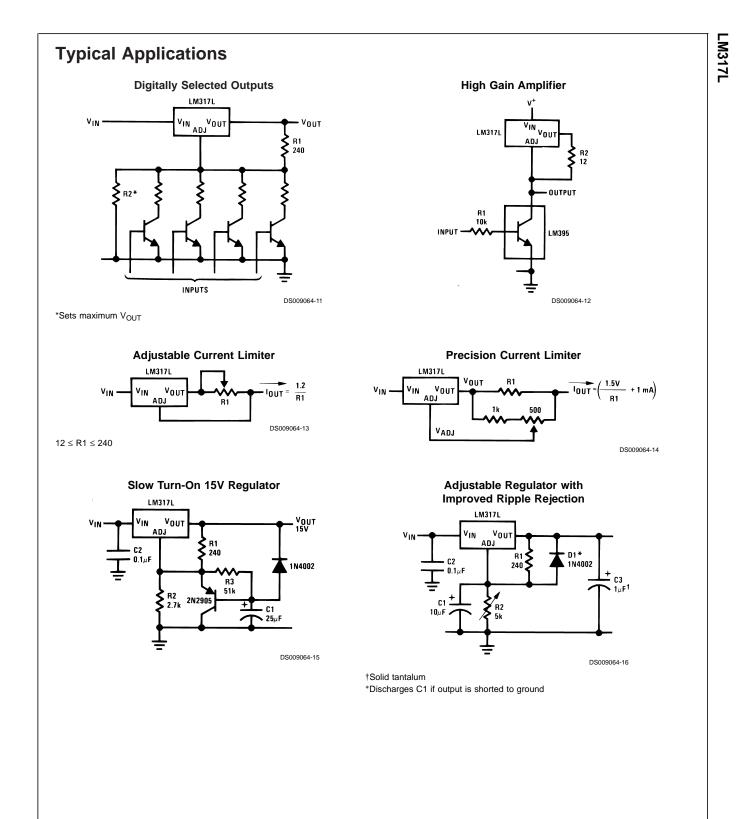
Exposing the LM317L micro SMD package to bright sunlight may cause the $V_{\sf REF}$ to drop. In a normal office environment of fluorescent lighting the output is not affected. The LM317

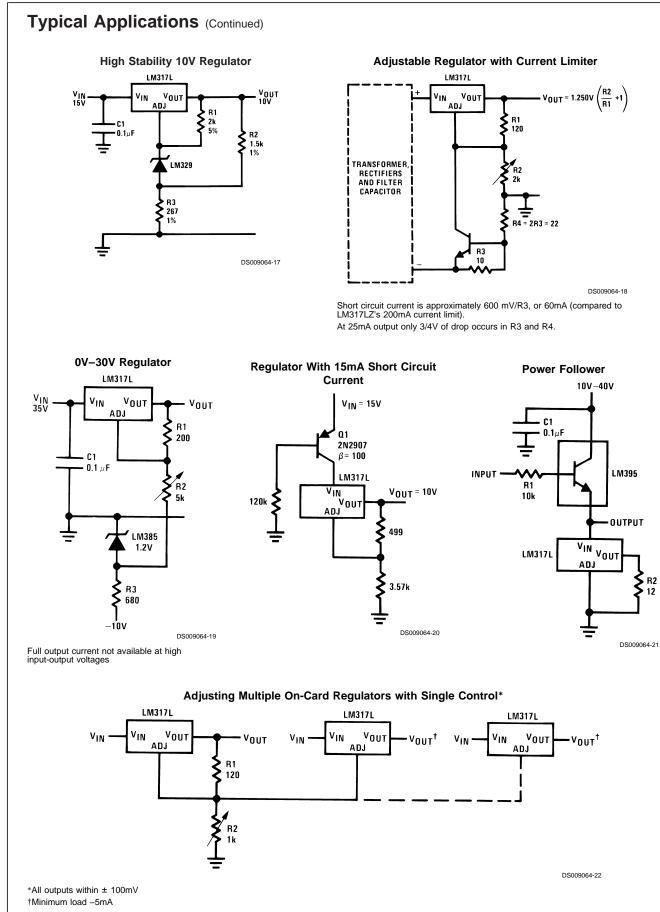
micro SMD does not sustain permanent damage from light exposure. Removing the light source will cause LM317L's $V_{\sf REF}$ to recover to the proper value.

LM317L

Schematic Diagram







LM317L Typical Applications (Continued) 1.2V–12V Regulator with Minimum Program Current 100mA Current Regulator 50mA Constant Current Battery Charger for Nickel-Cadmium LM317L Batteries LM317L VOUT v_{IN} VIN LM317L ADJ 15V VIN VOUT 24 · Vout* VIN v_{iN} Vout A D.J C1 R1 ξ ADJ **0.1** μF 12 R1 1.2k ξ DS009064-25 LOAD R2 20k DS009064-23 DS009064-24 *Minimum load current ~ 2 mA 5V Logic Regulator with Electronic Shutdown* **Current Limited 6V Charger** LM317L LM317L V_{IN} 7V–35V V_{IN} 9v то зоv VOUT VIN Vout v_{IN} ADJ ADJ **Š**^{R1} **>**240 240 C1 C2 0.1µF 0.1μF R2 720 2N2219 1000 μ F* 1.1k TTL 100 1k 2N2222 **₹** R1 10* 10* DS009064-26

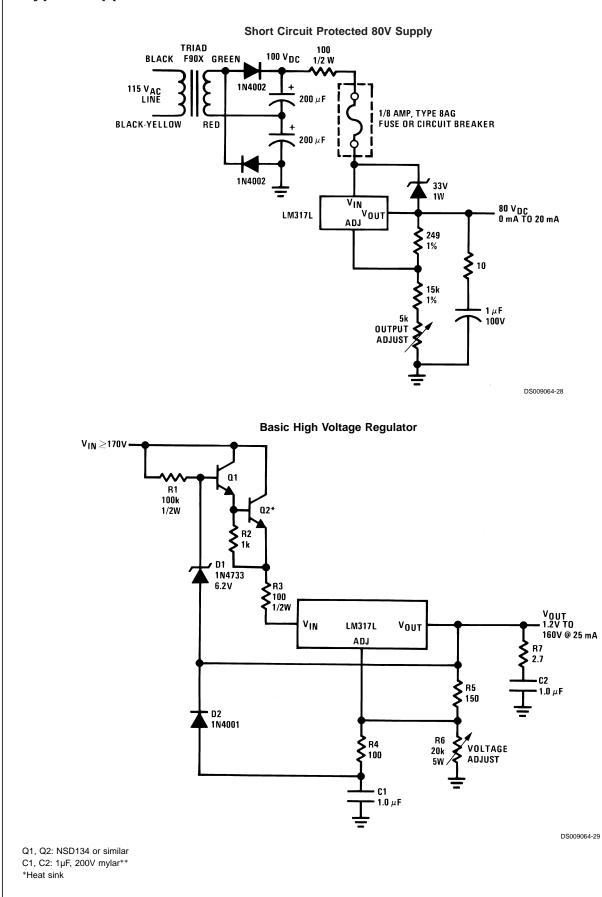
*Minimum output $\approx 1.2V$

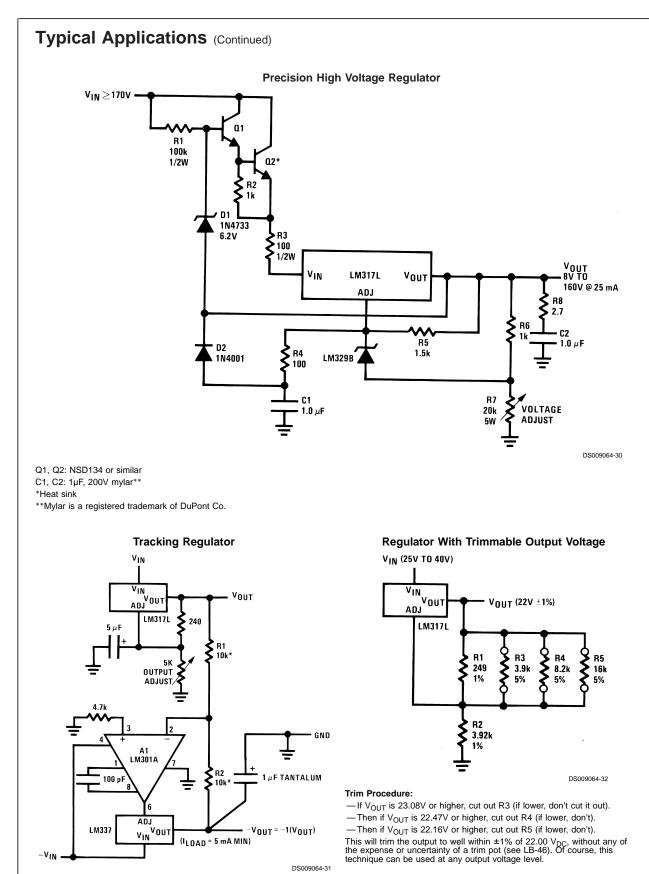
*Sets peak current, I_{PEAK} = 0.6V/R1

 $^{**}1000\mu F$ is recommended to filter out any input transients.

DS009064-27

Typical Applications (Continued)

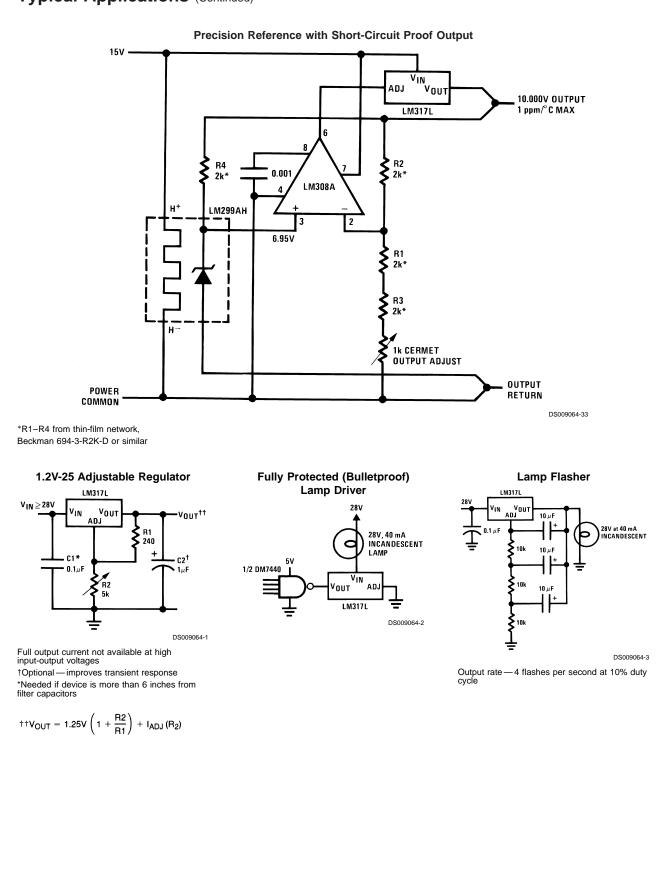


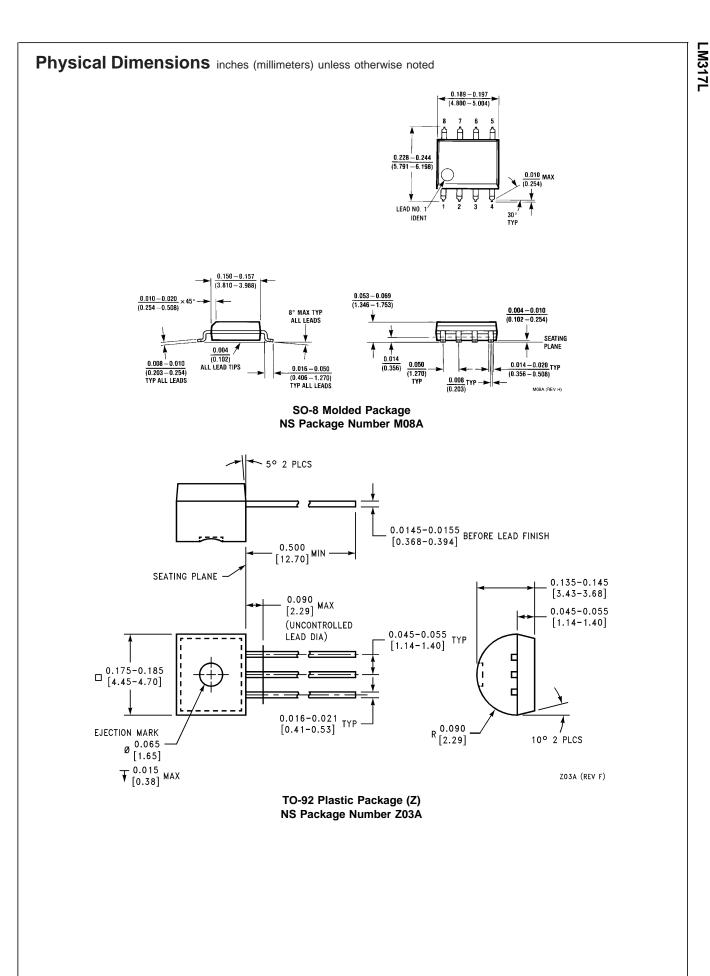


A1 = LM301A, LM307, or LF13741 only

R1, R2 = matched resistors with good TC tracking

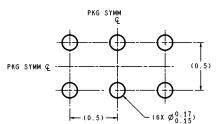
Typical Applications (Continued)



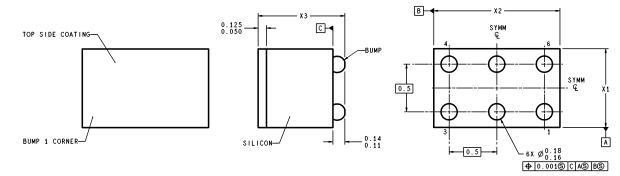


LM317L

Physical Dimensions inches (millimeters) unless otherwise noted (Continued)



LAND PATTERN RECOMMENDATION



DIMENSIONS ARE IN MILLIMETERS

BPA06XXX (Rev B)

NOTE: UNLESS OTHERWISE SPECIFIED.

1. EPOXY COATING

2. 63Sn/37Pb EUTECTIC BUMP.

3. RECOMMEND NON-SOLDER MASK DEFINED LANDING PAD.

4. PIN 1 IS ESTABLISHED BY LOWER LEFT CORNER WITH RESPECT TO TEXT ORIENTATION PINS ARE NUMBERED COUNTERCLOCKWISE.

5. XXX IN DRAWING NUMBER REPRESENTS PACKAGE SIZE VARIATION WHERE $\rm X_1$ IS PACKAGE WIDTH, $\rm X_2$ IS PACKAGE LENGTH AND $\rm X_3$ IS PACKAGE HEIGHT.

6. REFERENCE JEDEC REGISTRATION MO-211, VARIATION BC.

 $\begin{array}{l} \mbox{6-Bump micro SMD} \\ \mbox{NS Package Number BPA06HPB} \\ \mbox{X}_1 = 0.955 \quad \mbox{X}_2 = 1.615 \quad \mbox{X}_3 = 0.850 \end{array}$

Notes

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